

SPECIFICATIONS

Output Frequency

Tuning range:	6780 - 7880 MHz	
Frequency step size:	100 kHz	
Frequency stability and accuracy:	Same as input	
Phase noise in dBc/Hz (typ.):	L(100 Hz)	-75
	L(1 kHz)	-80
	L(10 kHz)	-80
	L(100 kHz)	-95
	L(1 MHz)	-120
Spurious:	-60 dBc	
Harmonics (typ.):	-20 dBc	
Power out (min.):	13 dBm	
Output power variation (freq. & temp.):	3 dB	
Load VSWR:	2:1	

Input Frequency

Input reference frequency:	5 or 10 MHz
Input ref. freq. level:	0 dBm \pm 3dB

Frequency Tuning / Alarm

Frequency control:	Serial RS-232 / RS-422 / RS-485
Acquisition time:	100 msec
Phase-lock indicator:	High = lock
	(Open collector or TTL compatible)

DC Power

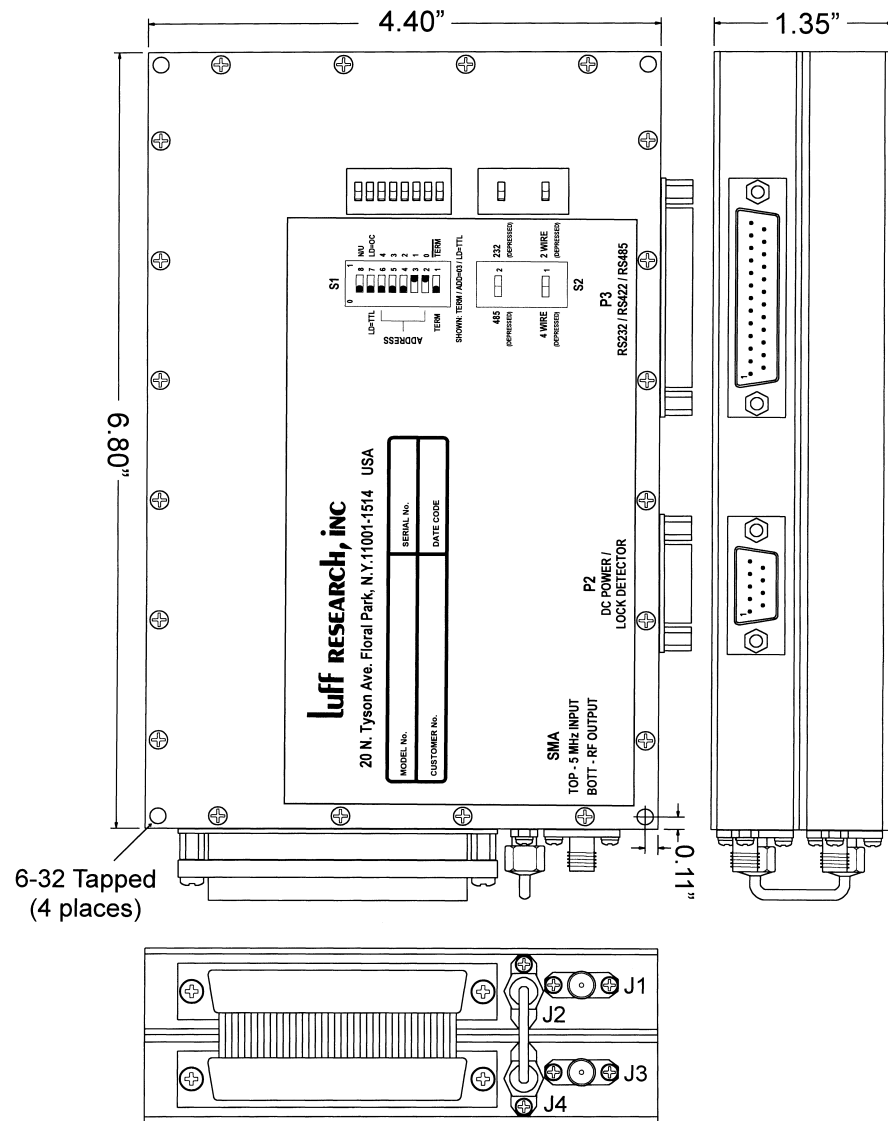
+5.2VDC (min) +7.0VDC (max)	750 mA
+15.0VDC (min) +17.0VDC (max)	250 mA

Mechanical

RF connectors:	2 SMA (F)
Digital & DC connection:	DB25 (M) / DB9 (M)

Environment

Operating temperature range (surface):	0°C to 60°C
Storage temperature range:	-40°C to 85°C
Relative humidity (non-condensing):	90%RH @ 40°
Shock:	30 G / 10msec
Vibration:	4 G / 20 Hz - 20 kHz
EMI Considerations:	EMI Gasketing
	Filtered Connectors



Notes:

- J1 is for 5 MHz input reference.
J4 is for 10 MHz input reference.
- On 10 MHz input reference units, J1 & J2 are not supplied.

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PRODUCT DATA SHEET

TLS FREQUENCY SYNTHESIZER / DECODER

Model: TLSC67807880/100K

Rev. E

06/09/03