



4timing P/N: TCLP2-80M000-BVI010B

SPECIFICATIONS

Frequency	80.000 MHz
Supply Voltage (Vcc)	B = 3.3 VDC $\pm$ 5%
Input Current	50 mA Maximum
Storage Temperature	-55°C to 125°C
Controllable Frequency Option	VI = Voltage control: $\pm$ 5 ppm Minimum + Internal trimmer: $\pm$ 3 ppm Minimum
Control Voltage (Vc)	1.65 $\pm$ 1.5 VDC
Setability of Vc at Fnom, 25°C	1.65 $\pm$ 0.4 VDC
Frequency Stability vs Temp.	010 = $\pm$ 1 ppm
Temperature Range	B = -40°C to 85°C
Frequency Stability vs Vcc	$\pm$ 0.2 ppm Maximum / Vcc $\pm$ 5%
Frequency Stability vs Load	$\pm$ 0.2 ppm Maximum / 15 pF $\pm$ 10%
Aging	$\pm$ 1 ppm Maximum per year @25°C
Output Load	50 Ohms to Vcc - 2V or Thevenin Equivalent, Bias Required
Logic "1" / Logic "0" Level	(Vcc - 1.02V) Minimum / (Vcc - 1.63V) Maximum
Rise/Fall Time (Tr/Tf)	1 ns Maximum at 20% to 80% Vp-p
Start-up time	5 ms Maximum
Duty Cycle	45/55%
Phase Noise (Typ)	-85 dBc/Hz at 10Hz -115 dBc/Hz at 100Hz -135 dBc/Hz at 1KHz -145 dBc/Hz at 10KHz -150 dBc/Hz at 100KHz

OUTLINE DRAWING

