

# Temperature Compensated Crystal Oscillators



## CMOS OUTPUT MINIATURE (5x3.2x1.2 mm) TCXO IN LCC PACKAGE - TC53C Series

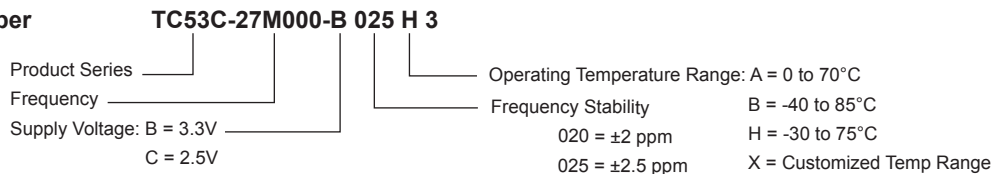
### FEATURES

- RoHS Compliant (Pb-Free), Tight Stability, Wide Frequency Range
- CMOS Output with Tri-state function
- Leadless Chip Carrier (LCC) Miniature Small Package, Industry de factor Standard Footprint
- Low Phase Noise, Low Jitter and Low Power Consumption

### SPECIFICATIONS

<b>Frequency Range</b>	4.000 MHz to 54.000 MHz
<b>Input Voltage (Vcc)</b>	1.8 - 3.3 VDC; B = 3.3V ± 10%; C = 2.5VDC ± 10%; D = 1.8VDC ± 10%
<b>Input Current</b>	7 mA Maximum / 1 uA Max Standby
<b>Storage Temperature</b>	-40°C to 125°C
<b>Frequency Stability vs Temp.</b>	020 = ±2 ppm; 025 = ±2.5 ppm (not all combination is available)
<b>Temperature Range</b>	A = 0°C to 70°C; B = -40°C to 85°C; H = -30°C to 75°C
<b>Standard Stability</b>	025H = ±2.5 ppm / -30°C to 75°C
<b>Frequency Stability vs Vcc</b>	±0.2 ppm Maximum / Vcc ± 10%
<b>Frequency Stability vs Load</b>	±0.2 ppm Maximum / 15 pF ±10%
<b>Aging</b>	±1 ppm Maximum per year @25°C
<b>Output Load</b>	CMOS 15 pF
<b>Logic "1" / Logic "0" Level</b>	0.9Vcc Minimum / 0.1Vcc Maximum
<b>Rise/Fall Time (Tr/Tf)</b>	5 ns Maximum
<b>Start-up time</b>	10 ms Maximum
<b>Duty Cycle</b>	3 = Tristate 55/45%
<b>Phase Noise (typ)</b>	-145 dBc/Hz at 10KHz
<b>Jitter (typ)</b>	3 ps, 1 Sigma RMS
<b>Tristate Function</b>	Input (Pin 1) High (> 0.7Vcc) or open: Output (Pin 3) active Input (Pin 1) Low (< 0.3Vcc): Output disabled in high impedance

### Creating a Part Number



### OUTLINE DRAWING

