



HCMOS Digi-TCXO/VCTCXO IN 14 PIN DIP COMPATIBLE PACKAGE - DTCTC Series

FEATURES

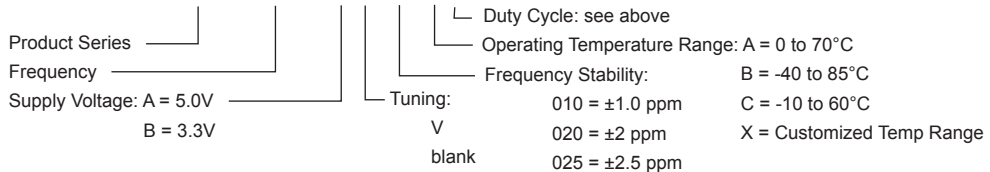
- Very Tight Frequency Stability over Wide Temperature Range
- Available with Voltage Control for Electric Frequency Adjustment
- HCMOS/TTL Compatible, Low Phase Noise
- Hermetically Sealed Package, Industry de factor Standard Footprint

SPECIFICATIONS

Frequency Range	1.5 MHz to 51.2 MHz
Standard Frequency	10, 12.8, 13.0, 16.384, 20.0, 26.0, 32.0, 36.864 MHz
Supply Voltage (Vcc)	A = 5.0 VDC \pm 5%; B = 3.3 VDC \pm 5%
Input Current	25 mA Max (5.0V); 20 mA Max (3.3V)
Storage Temperature	-40°C to 105°C
Controllable Frequency Option	V = Voltage control: \pm 5 ppm Typ, Positive, 10% Linearity
Control Voltage (Vc)	0.5 - 4.5 VDC for Vcc = 5 VDC; 0.3 - 3.0 VDC for Vcc = 3.3 VDC
Setability of Vc at Fnom, 25°C	Vc = 1/2 Vcc
Frequency Stability vs Temp. Temperature Range	003 = \pm 0.3 ppm; 005 = \pm 0.5 ppm; 010 = \pm 1 ppm A = 0°C to 70°C; B = -40°C to 85°C; C = -10°C to 60°C; D = -20°C to 70°C
Frequency Stability vs Vcc	\pm 0.3 ppm Maximum / Vcc \pm 5%
Frequency Stability vs Load	\pm 0.3 ppm Maximum / \pm 2 pF
Aging	\pm 1 ppm Maximum per year @25°C
Phase Noise (20MHz)	-85 dBc/Hz at 10Hz; -110 dBc/Hz at 100Hz -130 dBc/Hz at 1KHz; -135 dBc/Hz at 10KHz
Output Load	15 pF HCMOS
Logic "1" / Logic "0" Level	0.9Vcc Minimum / 0.1Vcc Maximum
Rise/Fall Time (Tr/Tf)	5 ns Maximum
Duty Cycle	0 = No tristate 60/40%; 2 = No tristate 55/45%

Creating a Part Number

DTCTC-20M000-A V 010 B 0



OUTLINE DRAWING

